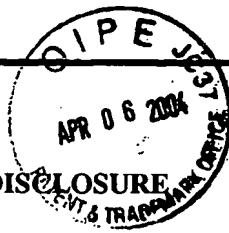


FORM PTO/SB/08A/B (10-01)  
Substitute for PTO-1449A/B

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)



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Applicant(s)	Tarek Kaylani et al.
Group Art Unit	<del>unknown</del> 2611
Examiner Name	<del>unknown</del> Tse, Young

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EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - Kind Code <sup>2</sup> (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
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**OTHER DOCUMENTS**

EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
/YT/		D. CHEN, "A Power and Area Efficient CMOS Clock/Data Recovery Circuit for High-Speed Serial Interfaces", IEEE Journal of Solid-State Circuits, Vol. 31, No. 8, August 1996, pages 1170-1176, New York
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EXAMINER SIGNATURE	/Young Tse/	DATE CONSIDERED	06/09/2007
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